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U.S. PATENT DOCUMENTS					
Evaminar	Cite No.1	Document Number Number-Kind Code ² (if known)	D. b.C Con Data		Pages, Columns, Lines, Where
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Examiner	0#4	Foreign Patent Document	Publication	Name of Patentee or	Pages, Columns, Lines,	
Initials*	Cite No. ¹	Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Date MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages Or Relevant Figures Appear	Τe
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PTO/SB/08b (07-09)

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}				Application Number	10/589,399	
11	IFORMATIC	ON DISC	CLOSURE	Filing Date	August 14, 2006	
S	STATEMENT BY APPLICANT			First Named Inventor	Guo-Quan LU	
		• •		Art Unit	1793	
	(Use as many sheets as necessary)			Examiner Name	Takeuchi	
Sheet	2	of	2	Attorney Docket Number	124617-00118	

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Cite No. ¹	the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
Α	Zhang, et al. "Nanoscale Silver Sintering for High-Temperature Packaging of Semiconductor Devices," TMS (The Minerals & Materials Society), 2004, 5 pp.	
В	Bai, Guofeng, "Low Temperaturd Sintering of Nanoscale Silver Paste for Semiconductor Device Interconnection," Dissertation, Copyright 2005, 196 pp.	
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	No. ¹	A Zhang, et al. "Nanoscale Silver Sintering for High-Temperature Packaging of Semiconductor Devices," TMS (The Minerals & Materials Society), 2004, 5 pp. Bai, Guofeng, "Low Temperaturd Sintering of Nanoscale Silver Paste for Semiconductor

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